

WHAT IS CLAIMED IS:

- 1 1. A method comprising the steps of:
 2 identifying a first portion of multimedia data associated with video content;
 3 identifying a second portion of the multimedia data associated with audio content,
 4 wherein the audio content is associated with the video content;
 5 processing the first portion to generate video data;
 6 processing the second portion to generate audio data;
 7 transferring the audio data to memory; and
 8 delaying the a transfer of audio data from memory to an audio output, wherein the delay is
 9 associated with a difference in a first time required to process the first portion to a
 10 second time needed to process the second portion;
- 1 2. The method of claim 1 further comprising the steps of:
 2 providing the video data at a video output after the step of delaying.
- 1 3. The method as in Claim 1, wherein processing the first portion includes performing de-
 2 interlacing on the first portion.
- 1 4. The method as in Claim 1, wherein processing the first portion includes digitizing the
 2 first portion.
- 1 5. The method as in Claim 1, wherein processing the second portion includes digitizing the
 2 second portion.
- 1 6. The method as in Claim 1, wherein the delay is determined according to a mode of
 2 operation.

1 7. The method as in Claim 6, wherein the mode of operation includes receiving multimedia
2 data associated with a broadcast analog audio and video program.

1 8. The method as in Claim 6, wherein the mode of operation includes receiving multimedia
2 data associated with a broadcast digital audio and video program.

1 9. The method of claim 8, wherein the broadcast digital audio and video program is
2 broadcast as part of a packetized multiplexed signal.

1 10. The method of claim 9, wherein the packetized multiplexed signal is an MPEG type
2 signal.

1 11. The method as in Claim 6, wherein the mode of operation includes receiving multimedia
2 data associated with picture-in-picture video to be generated, wherein a digital audio and
3 video program is mixed with a representation of an analog video program.

1 12. The method as in Claim 6, wherein the mode of operation includes receiving multimedia
2 data associated with picture-in-picture video to be generated, wherein an analog audio
3 and video program is mixed with a representation of an analog video program.

1 13. The method as in Claim 1, wherein the step of transferring includes transferring the audio
2 data to audio memory using a first channel of a data port.

1 14. The method as in Claim 11, wherein the step of transferring includes transferring the
2 audio data to the audio output using a second channel of a data port.

1 15. The method as in Claim 1, further including the step of adjusting a rate of transfer of the

2 audio data to synchronize the audio data with the video data.

16. A system comprising:
a data processor having an I/O buffer;
a memory having an I/O buffer coupled to the I/O buffer of the data processor, the
memory storing code to control said data processor to:
determine a delay amount, wherein the delay is associated with a difference
between an amount of time to process a portion of video data to amount of
time to process a portion of audio data associated with the video data;
assert a transfer of processed audio data to memory through a data port;
assert, after waiting the delay amount, a transfer of a representation of the
processed audio data from memory to an audio output through a data port;
a video processor to process the portion of video data to generate processed video data;
an audio processor to process the portion of audio data to generate processed audio data;
a memory controller comprising:
a first register field to enable a transfer of the processed audio data to memory;
and
a second register field to enable a transfer of the representation of the processed
audio data from memory to an audio output.

17. The system as in Claim 16, wherein said video processor further includes:
an analog video encoder to:
digitize analog video data to generate digital video data;
decode digital video data to analog video;
provide digital video data related to interlaced video data to a digital video
processor;
provide the video data to the video output; and
said digital video processor to de-interlace digital video data related to interlaced video
data.

18. The system as in Claim 17, wherein the digital video processor is to further process
MPEG transport packets into video data.

1 19. The system as in Claim 16, wherein the audio processor is to further digitize analog audio
2 data to digital audio data.

1 20. The system as in Claim 16, wherein the data port is coupled to the video processor.

1 21. The system as in Claim 16, wherein the data port is coupled to the video processor to
2 transfer the processed audio data to and from memory.

1 22. The system as in Claim 16, further including a host bus interface unit, with an I/O bus
2 coupled to the I/O bus of the data processor, to provide access to memory to the data port.

1 23. The system as in Claim 16, wherein the delay is determined according to a mode of
2 operation.

1 24. The system as in Claim 23, wherein the mode of operation includes processing
2 multimedia data associated with analog audio and video content to generate the portion of
3 audio data and the portion of video data.

1 25. The system as in Claim 23, wherein the mode of operation includes processing
2 multimedia data associated with digital audio and video content to generate the portion of
3 audio data and the portion of video data.

1 26. The system as in Claim 23, wherein the mode of operation includes generating picture-in-
2 picture video data by mixing multimedia data associated with digital audio and video
3 content with video data associated with an analog video content.

1 27. The system as in Claim 23, wherein the mode of operation includes generating picture-in-
2 picture video data by mixing multimedia data associated with analog audio and video
3 content with video data associated with an analog video content.

1 28. The system as in Claim 16, wherein memory is to further store code to:
2 assign a first address associated with a first portion of memory to store processed audio
3 data;
4 assigning a second address associated with a second portion of memory to read a set of a
5 representation of the processed audio data.

1 29. The system as in Claim 28, wherein a rate of transfer of processed audio data is altered to
2 keep an amount of stored data between the first and second addresses constant.

1 30. The system as in Claim 16, wherein the data port is a multiple channel data access port
2 and further includes:
3 a first FIFO channel for transferring the processed audio data to memory; and
4 a second FIFO channel for transferring the representation of the processed audio
5 data from memory.

1 31. The system as in Claim 30, further including a third FIFO channel for performing general
2 functions.

1 32. The system as in Claim 31, wherein the first and second port include separate controls for
2 initiating data transfers.

33. A computer readable medium tangibly embodying a plurality of instructions, the plurality of instructions for:

storing a delay amount, wherein the delay amount is associated with a difference between an amount of time to process a portion of video data to the amount of time to process a portion of audio data, wherein the portion of audio data is associated with the video data;

assigning a first address associated with a portion of memory to write a set of processed audio data;

assigning a second address associated with a portion of memory to read a set of stored audio data, wherein the stored audio data is associated with the processed audio data;

asserting a transfer of processed audio data to memory through a data port, wherein the transfer implement the delay amount.

34. The computer readable medium as in Claim 33, further including adjusting a rate of transfer of stored audio data to maintain a constant amount of data between the first and the second addresses.

35. The computer readable medium as in Claim 33, wherein the computer readable medium is coupled to controls of the data port.

36. The computer readable medium as in Claim 33, further including determining the delay by analyzing presentation times associated with the portion of audio data and the portion of video data.